

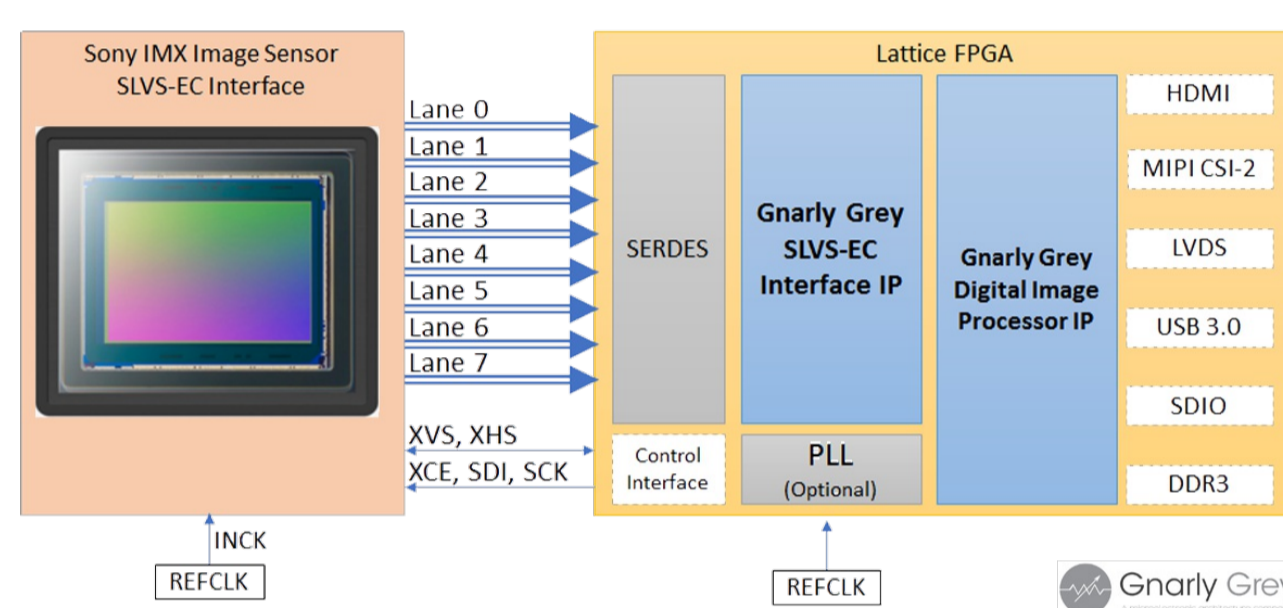
- SLVS-EC LatticeECP5 Development Platform
- UPduino FPGA Arduino board

Gnarly Grey SLVS-EC Development Platform

The Gnarly Grey SLVS-EC development platform provides a complement to the Sony CUE1 Development Platform for SLVS-EC image sensors using LatticeECP5 FPGAs and Gnarly Grey SLVS-EC Interface IP.

Gnarly Grey SLVS-EC Development Platform

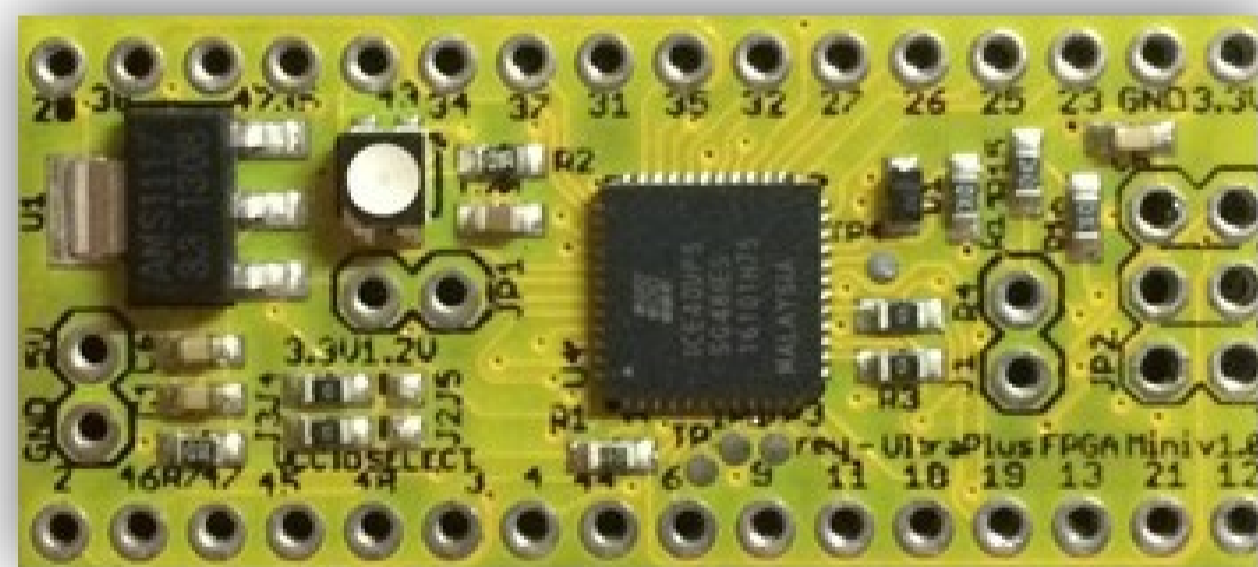
- Interfaces to Sony SLVS-EC Image Sensor Boards compatible with the Sony CUE1 development platform
- Provides interfacing to two Lattice ECP5 FPGAs with 8 channels of SLVS-EC inputs at up to 5Gbps per input
- Two HDMI outputs at 4k30
- USB 3.0 and USB 2.0
- Two general purpose trace matched LVDS or GPIO headers for general development needs



Gnarly Grey UPduino Board

Arduino has been a great tool for rapid development using microcontrollers. Complementing microcontroller development with the performance and IO capabilities of an FPGA with similar cost, power and size metrics provides the ultimate nirvana. The Gnarly Grey UPduino Board provides this on the latest and greatest mobile FPGA from Lattice called UltraPlus. The Ultraplus FPGA is unique in that it provides the power profile of a microcontroller with the amount of block ram found in large FPGAs. This is a great board for IO expansion, data buffering, and low latency requirements typically not serviceable by a microcontroller.

[Order now – Only \\$7.99 w/free shipping via Paypal!](#)



Gnarly Grey UPduino Board Features

- Lattice UltraPlus FPGA
 - 5.3K LUTs, 1Mb SPRAM, 120Kb DPRAM, 8 Multipliers
- Stacks onto Arduino Nano and Arduino Pro Mini Boards (same header spacing)
- 34 GPIO on 0.1" headers
- SPI Flash, RGB LED, 3.3V and 1.2V Regulators
- Open source schematic and layout using free Eagle PCB design tools
 - [Download Schematic & Layout](#)
 - [Schematic PDF](#)
 - [FT232H Programming Instructions](#)
 - [Raspberry Pi Programming Instructions](#)
 - [Raspberry Pi Programming Files](#)
 - [RGB LED Example Project](#)
 - [Raspberry Pi IO Expansion](#)