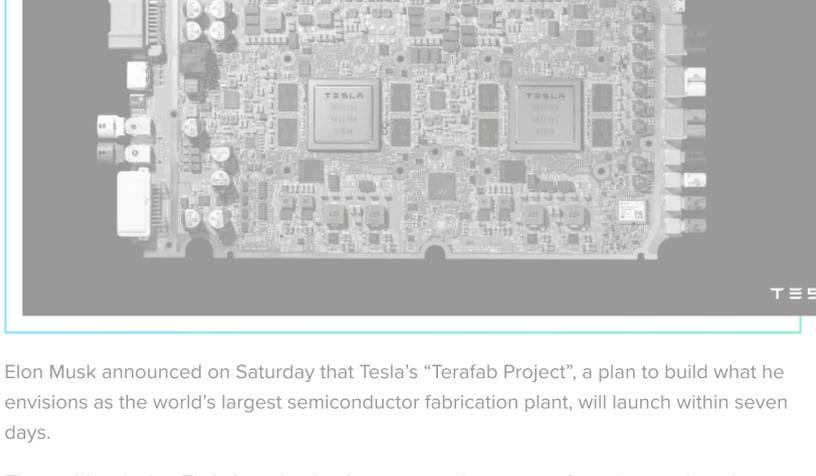


TESLA

# Tesla's Terafab chip fab ambitions ignore its total lack of semiconductor experience

Fred Lambert | Mar 16 2026 - 7:39 am PT | 112 Comments



Elon Musk announced on Saturday that Tesla's "Terafab Project", a plan to build what he envisions as the world's largest semiconductor fabrication plant, will launch within seven days.

The problem is that Tesla has absolutely zero experience manufacturing semiconductors, and its track record in the closest comparable venture, building its own battery cells, should give anyone pause.

## From battery cells to chips: Tesla's manufacturing track record

Tesla's decision to get into chip manufacturing echoes a similar strategic move from six years ago, when the company unveiled its 4680 battery cell at Battery Day in September 2020. Back then, Musk made bold promises: Tesla would build 100 GWh of in-house cell production capacity by 2022, cut battery costs by 56%, and use the savings to deliver a \$25,000 electric vehicle.

None of that happened on schedule. The 100 GWh target for 2022 was missed by a wide margin. By early 2025, Tesla's actual 4680 production was estimated at roughly 20 GWh per year — five years after the announcement and still a fraction of the original 2022 target. The dry electrode process that was central to Tesla's cost reduction claims proved far more difficult than anticipated. For years, Tesla could only apply it to the anode, while the cathode relied on traditional wet-electrode materials from external suppliers.

The cells themselves didn't live up to the original promises either. Battery Day claimed the new cell design would deliver 5x more energy, 6x more power, and a 16% range increase. In practice, the 4680 cells went into a single product, the Cybertruck, **which has become a commercial failure**. Tesla's 4680 battery supply chain has since **collapsed, with supplier L&F Co. writing down its Tesla deal by 99%**.

To be fair, Tesla recently achieved some milestones with 4680 cells. The company says it reached the lowest cost per kWh of any cell it produces by the end of 2024, and it has **put 4680 cells back in the Model Y**. But the broader picture is that Tesla spent six years trying to scale battery cell production — a discipline where it at least had adjacent expertise in battery pack engineering — and still hasn't delivered on most of its original promises.

Semiconductor fabrication is orders of magnitude more complex.

## Tesla gutted its own chip talent

The comparison to battery cells is imperfect, of course. Battery cell manufacturing and semiconductor fabrication are fundamentally different disciplines. But that only makes the chip fab challenge harder, because Tesla has even less institutional knowledge in semiconductors than it had in electrochemistry when it launched the 4680 program.

Tesla did build a strong chip *design* team over the years. The company hired Jim Keller, the legendary chip architect, in 2016, and later brought on Peter Bannon from Apple's PA Semi team. That team designed Tesla's custom Autopilot inference chips — HW3 and HW4 — as well as the Dojo training chips. It was a genuine achievement.

But much of that talent is gone. Keller left back in 2018. Ganesh Venkataramanan, who led the Dojo project, **departed in late 2023** amid reported issues with the next-generation Dojo 2 chip. Then in August 2025, **Musk killed the entire Dojo project**, and Peter Bannon — the architect in charge of all custom silicon at Tesla — left the company. About 20 Dojo team members followed Venkataramanan to his new startup, DensityAI, and the remaining workers were reassigned.

That was Tesla's chip *design* talent. Chip *manufacturing* talent is an entirely different workforce — process engineers specializing in lithography, etching, chemical-mechanical planarization, yield management, EUV equipment operation, and dozens of other disciplines that Tesla has never employed. Tesla is now recruiting for these roles, but building a team capable of running a leading-edge 2nm fab from job postings is a far cry from the decades of institutional knowledge that TSMC, Samsung, and Intel have accumulated.

## Musk's cleanroom comments don't inspire confidence

Musk's own statements about chip manufacturing suggest he may not fully appreciate the complexity of what he's proposing. In January, he claimed that the semiconductor industry is "getting clean rooms wrong" and bet that Tesla would build a 2nm fab where he can "eat a cheeseburger and smoke a cigar."

His argument is that wafers should be fully contained at all times, eliminating the need for a traditional cleanroom environment. Semiconductor experts immediately pushed back. Modern leading-edge fabs operate at ISO Class 1-3 cleanroom standards, where even human breath can introduce contaminants. Chips at the 2nm node are made of particles, many smaller than a human hair, and require a cleanroom environment with air filtered to 0.1 micrometers. Tesla's plan to build a 2nm fab in a cleanroom, has led chip makers to question Musk's confidence in 2025. "It is not clear what TSMC is doing to match Tesla's ambitions," says a source.

Even Nvidia CEO Jensen Huang publicly warned that Tesla's plan to build a 2nm fab is "just build the cleanroom" and "just build the fab" is not what TSMC does for a living. TSMC's semiconductor manufacturing process is a \$100 billion business, designed to produce billions of chips per month. Intel, the world's largest semiconductor manufacturer, has spent billions of dollars building a 2nm fab in Arizona. Samsung, another semiconductor giant, has spent billions of dollars building a 2nm fab in South Korea. Tesla's plan to build a 2nm fab in a cleanroom, has led chip makers to question Musk's confidence in 2025. "It is not clear what TSMC is doing to match Tesla's ambitions," says a source.

Tesla wants to build a 2nm fab under one roof — something no company, including TSMC, currently does at this scale. The fab would produce billions of chips per month. Intel, the world's largest semiconductor manufacturer, has spent billions of dollars building a 2nm fab in Arizona. Samsung, another semiconductor giant, has spent billions of dollars building a 2nm fab in South Korea. Tesla's plan to build a 2nm fab in a cleanroom, has led chip makers to question Musk's confidence in 2025. "It is not clear what TSMC is doing to match Tesla's ambitions," says a source.

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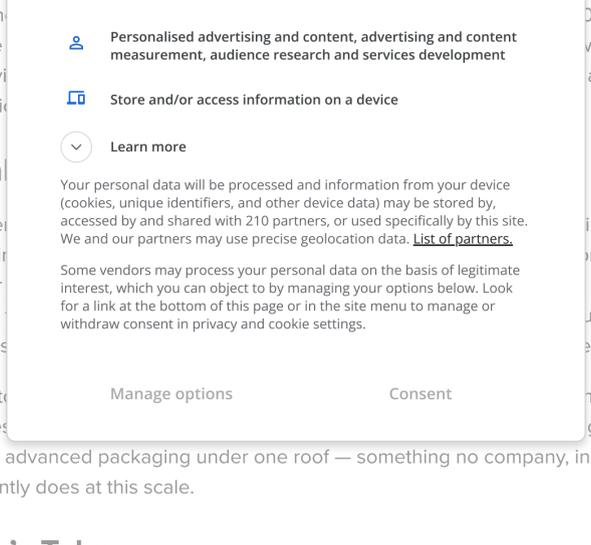
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## Tesla

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